

**REMARKS**

Claims 1-17 were rejected under 35 U.S.C. 112, second paragraph, as being indefinite. Applicant has amended the claims to address the issues noted by the Examiner.

Claims 1-17 were rejected under 35 U.S.C. 102(b) as being anticipated by Reefman or van der Kam. Claims 1-17 were rejected under 35 U.S.C. 102(e) as being anticipated by Page.

Claims 1, 5, 9 and 14 have been amended to more particularly claim the invention and distinguish over the cited prior art references.

In claim 1, Applicant claims “means for using bits in a first set of nodes to address one of said memory locations and receive first content; means for using bits in a second set of nodes, **after applying an address offset to the bits which is not applied against the bits in the first set of nodes**, to address one of said memory locations and receive second content” (emphasis added). The cited prior art teach using the bits of the register as an address for addressing a memory to retrieve data for use in the accumulation. There is no teaching or suggestion in the cited prior art for applying a unique address offset to the data from the second set of nodes in order to change the addressed memory location. Such an address offsetting process is useful in addressing the issue discussed by Applicant at page 8 lines 10-18 where a same bit pattern, present in two different sets of the register (as a result of shifting), needs to produce (retrieve) a different content from the memory. The address offset uniquely used in the second set, as opposed to and not with the first set, allows for the generation of two different memory address locations for that same shifted bit pattern, and thus allows for the retrieval from memory of two different content values for accumulation (from two different memory addresses).

In view of the foregoing, Applicant submits that claim 1 is not anticipated by the cited prior art.

Similar amendments have been made to claims 5, 9 and 14. Applicant submits that these claims distinguish over the cited prior art for at least the reasons recited above with respect to claim 1.

CUSTOMER NO. 30430

PATENT APPLICATION

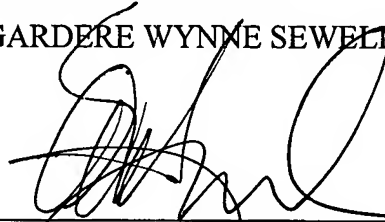
Docket No. 03-MW-021

Applicant respectfully submits that the application is now in condition for favorable action and allowance.

Dated: October 2, 2007

Respectfully submitted,

GARDERE WYNNE SEWELL LLP

A handwritten signature in black ink, appearing to read 'AS', is written over a horizontal line.

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